

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		(operating adj1 mode\$1) and ((address adj1 size) and (operand adj1 size))	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:09
		(operating adj1 mode) near4 indicators	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:09
L1	278	(712/229).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 13:58
L2	205	(712/43).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 13:58
L3	2	(address\$3 near4 siz\$3) near4 (enabl\$3 near4 pag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 13:59
L4	2	(address\$3 near4 siz\$3) with (enabl\$3 near4 pag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 13:59
L5	15	(address\$3 near4 siz\$3) same (enabl\$3 near4 pag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 14:00
L6	40	(address\$3 near4 siz\$3) same (enabl\$3 with pag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 14:01
L7	4	(address\$3 adj1 siz\$3) same (enabl\$3 with pag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 14:02
L8	35	(address\$3 adj1 siz\$3) same (enabl\$3 same pag\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/16 14:02
S1	357	mode adj1 indicators	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:07

S2	63	(operating adj1 mode) and (mode adj1 indicators)	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:08
S3	63	(operating adj1 mode) and (mode adj1 indicators)	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:08
S4	26	processor and (operating adj1 mode) and (mode adj1 indicators)	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:08
S5	8	(operating adj1 mode) near4 (mode adj1 indicators)	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:08
S6	231	(operat\$4 adj1 mod\$2) near4 (mod\$2 adj1 bit\$1)	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:09
S7	113	(operating adj1 mode) and (("16" adj1 bits) near4 ("32" adj1 bits))	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:09
S8	81	(operating adj1 mode\$1) and (mode adj1 indicators)	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:09
S9	42	processor and (operating adj1 mode\$1) and (mode adj1 indicators)	US-PGPUB; USPAT; JPO; DERWENT	OR	OFF	2004/05/26 15:09
S10	3	(segment adj1 selector\$1) near4 (entries)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:13
S11	0	(adjacent adj1 memory adj1 locations) near4 (64?bit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:16
S12	0	(adjacent adj1 memory) near4 (64?bit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:15
S13	0	(adjacent adj1 memory) near4 ("64" adj1 bit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:16

S14	35	(memory adj1 locations) near4 ("64" adj1 bit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:15
S15	335	adjacent adj1 memory adj1 locations	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:16
S16	0	(adjacent adj1 memory adj1 locations) near4 (large adj1 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:31
S17	73	(adjacent adj1 memory adj1 locations) near4 (data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:18
S18	27	(floating adj1 point) near4 (memory adj1 locations)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:19
S19	0	(memory adj1 locations) near4 (large adj1 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:32
S20	7	(memory adj1 locations) with (large adj1 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:36
S21	26	(memory adj1 locations) same (large adj1 data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:47
S22	120	(double adj1 word) near4 storage	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:52
S23	11	(double adj1 word) near4 storage near4 locations	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 15:52
S24	827	instruction adj1 translat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 16:02
S25	4	software near4 instruction adj1 translat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 16:02

S26	0	software adj1 instruction adj1 translat\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 16:02
S27	13	software near4 (instruction adj1 translat\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/05/26 16:02
S28	242	(712/229).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/28 07:59
S29	190	(712/43).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/06/28 07:59
S30	3289	pag\$3 near4 segment\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/28 08:00
S31	273	paging near4 segmentation	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/28 08:00
S32	0	(paging adj2 addresses) near4 (segmentation adj1 memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/28 08:01
S33	9	paging near4 (segmentation adj1 memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/28 08:21
S34	232	paging near2 segmentation	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/06/28 08:22
S35	267	(712/229).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/09 13:51
S36	198	(712/43).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/12/09 13:51

S37	272	(712/229).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/04/04 07:37
S38	203	(712/43).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/04/04 07:37



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